

## Brief Profile



**Brajesh Kumar Kaushik** received his Doctorate of Philosophy (Ph.D.) in 2007 from Indian Institute of Technology, Roorkee, India. He joined Department of Electronics and Communication Engineering, Indian Institute of technology, Roorkee, as Assistant Professor in December 2009; and since April 2014 he has been an Associate Professor. He has served as General Chair, Technical Chair, and Keynote Speaker of many reputed international and national conferences. Dr. Kaushik is a *Senior Member* of IEEE and member of many expert committees constituted by government and non-government organizations. He is Editor-in-Chief of International *Journal of VLSI Design & Communication Systems (VLSICS)*, AIRCC Publishing Corporation. He also holds the position of Editor of *Microelectronics Journal (MEJ)*, Elsevier Inc.; *Journal of Engineering, Design and Technology (JEDT)*, Emerald Group Publishing Limited; and *Journal of Electrical and Electronics Engineering Research (JEEER)*, Academic Journals. He has received many awards and recognitions from the International Biographical Center (IBC), Cambridge. His name has been listed in Marquis Who's Who in Science and Engineering® and Marquis Who's Who in the World®. Dr. Kaushik has been conferred with *Distinguished Lecturer* award of IEEE Electron Devices Society (EDS) to offer EDS Chapters with quality lectures in his research domain. His research interests are in the areas of high-speed interconnects, low-power VLSI design, memory design, carbon nanotube-based designs, organic electronics, FinFET device circuit co-design, electronic design automation (EDA), spintronics-based devices, circuits and computing, image processing, and optics and photonics based devices.